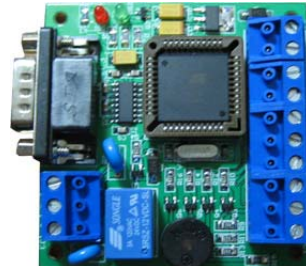


## Serial ASCII to Wiegand Converter

**MA1507** is designed to convert serial ASCII string to Wiegand data. The converter can automatically convert 96-bit EPC GEN 2 tag ID in Terse form into 50-bit Wiegand output. The 50-bit Wiegand output is computed from the last 48-bit data in the tag ID.



### 1. Specification

#### 1.1 Communications:

115200 BPS ASYNC, 8 bits, 1 Stop, No Parity.

### 2. Conversion Format

#### 2.1 Input Format

96-bit EPC GEN 2 tag ID in Terse form with CR HEX <0D> signals end of string.

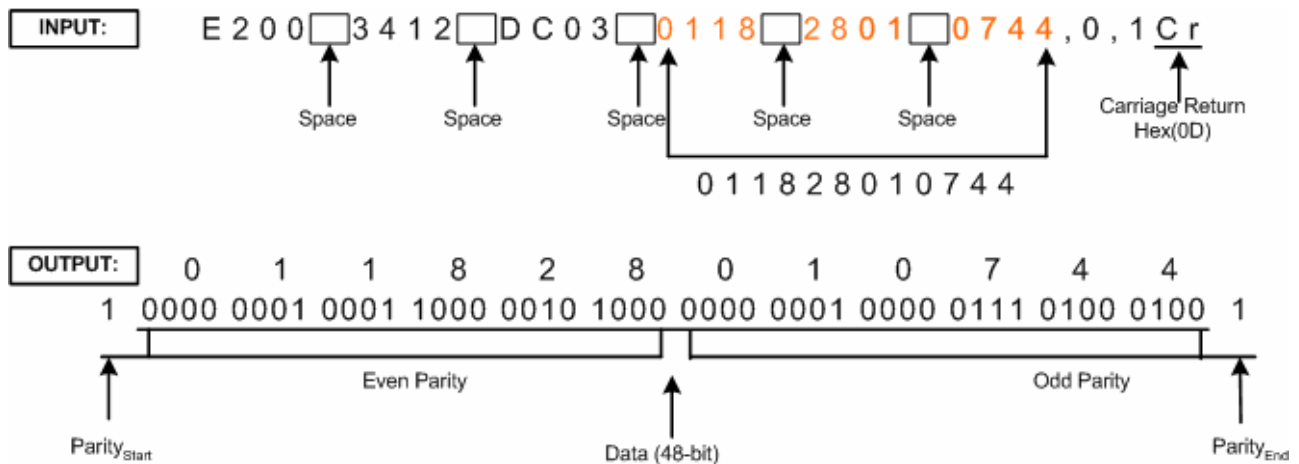
#### 2.2 Output Format

Standard 50-bit Wiegand format. The 50-bit Wiegand output is computed from the last 48-bit data in the tag ID.

## 2.3 Example

Input: E200 3412 DC03 0118 2801 0744,0,1

Output: 1 0000 0001 0000 0111 0100 0100 0



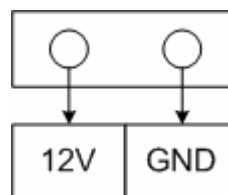
## 3. Connectors:

There are total 3 pluggable connectors and one DB9 male connector located on the MA1507 board serving different functions as described below.

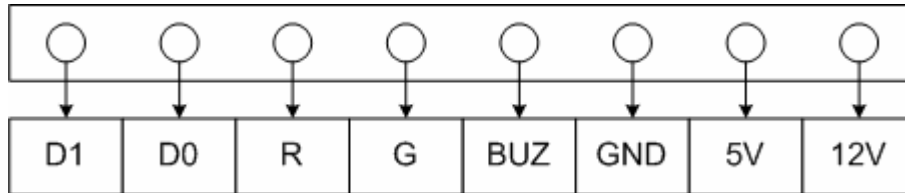
Connector Symbol	Description
J1	9V – 12V DC Power Supply
J2	Wiegand Output
J3	Reserved.

### 3.1 Connector J1

Connector J1 Port Assignment	
Designator	Description
12V	12V DC Power Supply In
GND	Power Supply Ground



## 3.2 Connector J2



Connector J2 Port Assignment	
Designator	Description
D1	D1
D0	D0
R	Reserved
G	Reserved
BUZ	Reserved
GND	GND
5V	5V Output
12V	12V Output

## 3.3 DB9 Male

DB9 Male Port Pin Assignment	
Designator	Description
1	Unused
2	TX data to terminal
3	RX data from terminal
4	Unused
5	Ground
6	Unused
7	Unused
8	Unused
9	Unused

